



091823405

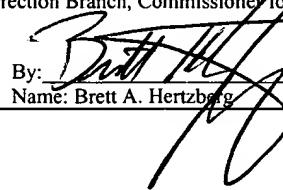
IPW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patentee: Hai Thanh Nguyen et al. Docket: 50019.61US01/P04968
Patent No.: US 6,880,099 B1
Issued: April 12, 2005
Title: METHOD AND APPARATUS NOT REQUIRING A CLOCK SIGNAL FOR SAMPLING AN INPUT SIGNAL

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Certificate of Correction Branch, Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450 July 5, 2005.

By: 
Name: Brett A. Hertzberg

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

We are transmitting herewith the attached:

- Transmittal Sheet in duplicate containing Certificate of Mailing
- Request for Certificate of Correction
- Certificate of Correction
- Charge Deposit Account 13-2725 in the amount of \$100.00 for Certificate of Correction for Applicants' Errors
- Return postcard

Please consider this a PETITION FOR EXTENSION OF TIME for a sufficient number of months to enter these papers, if appropriate. Please charge any additional fees or credit overpayment to Deposit Account No. 13-2725. A duplicate of this sheet is enclosed.

Merchant & Gould P.C.
P.O. Box 2903 Minneapolis, MN 55402-0903
206.342.6200

By: 
Name: Brett A. Hertzberg
Reg. No.: 42,660
BAH/ab

23552

PATENT TRADEMARK OFFICE



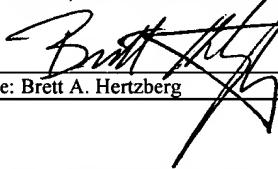
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No.: US 6,880,099 B1 Docket: 50019.61US01/P04968
Issue Date: April 12, 2005 Patentee: Hai Thanh Nguyen et al.
Title: METHOD AND APPARATUS NOT REQUIRING A CLOCK SIGNAL
FOR SAMPLING AN INPUT SIGNAL

CERTIFICATE UNDER 37 CFR 1.8:

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, with sufficient postage, in an envelope addressed to: Certificate of Correction Branch, Commissioner for Patents, P.O. Box 1450 Alexandria, Virginia 22313-1450 on July 5, 2005.

By: 
Name: Brett A. Hertzberg

REQUEST FOR CERTIFICATE OF CORRECTION

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United States patent. One copy of the text of the Certificate in the suggested form is enclosed.

As some of the errors listed are due to Applicants' mistake, please charge Deposit Account 13-2725 in the amount of \$100.00 to cover the Certificate fee.

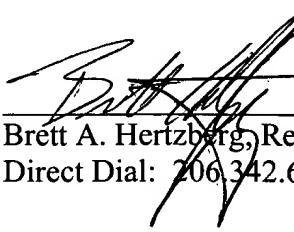
Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims, and re-examination is not required.

Respectfully submitted,

MERCHANT & GOULD
P.O. Box 2903
Minneapolis, Minnesota 55402-0903

07/08/2005 HTECKLU2 0000004 132725 6880099
01 FC:1811 100.00 DA

23552
PATENT TRADEMARK OFFICE


Brett A. Hertzberg, Reg. No. 42,660
Direct Dial: 206.342.6255

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : US 6,880,099 B1

PAGE 1 of 1

DATED : April 12, 2005

INVENTOR(S) : Hai Thanh Nguyen et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 8, Line 23, "reset signal (706, RS1). " should read --reset signal (706 RST)--

Column 10, Line 47, "Since many embodiment" should read --Since many embodiments--

Column 10, Line 58, "pulse width that are" should read" --pulse width that is--

Column 11, Line 37, "a timing delay circuit is arranged" should read --a timing delay circuit arranged--

Column 11, Line 41, "a comparator circuit is arranged" should read --a comparator circuit arranged--

Column 11, Line 44, "a sampling logic is arranged" should read --a sampling logic arranged--

Column 12, Line 29, "a timing delay circuit is arranged" should read --a timing delay circuit arranged--

Column 12, Line 33, "a comparator circuit is arranged" should read --a comparator circuit arranged--

Column 12, Line 36, "a sampling logic is arranged" should read --a sampling logic arranged--

Column 13, Line 9, "a digital representation the of" should read --a digital representation of--

Column 14, Line 5, "and an other logic" should read --and another logic--

Column 14, Line 18, "data point in response the" should read --data point in response to the--

MAILING ADDRESS OF SENDER:

Merchant & Gould P.C.
Attn: Brett A. Hertzberg
P.O. Box 2903
Minneapolis, MN 55402-0903

PATENT NO.

US 6,880,099 B1

Docket No.

50019.0061US01/P04968

No. of add'l copies

0

23552

PATENT TRADEMARK OFFICE